





**Department of Electronics & Communication Engineering** 

National Institute of Technology Silchar, Assam

## SERB Student Internship Program वृतिका / VRITIKA

- One month internship program, funded by Science & Engineering Research Board, Department of Science & Technology, Govt. of India.
- Opportunities to promising **UG/ PG students** from universities and colleges within India.
- Get exposure and hands-on research skill development experience in the trending area of Machine Learning Based Analysis for Predicting the Effect of Device Parameters on Performance of Nanoscale TFET.
- □ Organized by the Department of Electronics & Communication Engineering, National Institute of Technology Silchar, Assam.
- Get attractive benefits including internship certificate with complimentary food, travel & accommodation.

Who can apply?	How to apply?	Details of the Internship	Benefits of the Internship
Regular UG/PG/Ph.D level students pursuing their degree in ECE, EEE, EIE from University/ Institution within India. Skills Required: Basic knowledge of MOS device analysis, mathematical modelling and familiar with at least one programming language MATLAB/ Python.	Step 1: Download and complete the Annexure-I, II and III from https://drive.google.com/drive/fold ers/1ZXtWdCIdATdKk_g9ql6VUy MAVUpc5lFX?usp=sharing Step 2: Scan these documents and save them in PDF format. Step 3: Apply using the following google form: https://forms.gle/7pQafM9X xNormUdR7 Last Date of application: 03 <sup>rd</sup> Aug 2024.	Duration: 1 month (Offline/Physical mode) Date: 05/08/2024 – 04/09/2024 Research Topic: Machine Learning Based Analysis for Predicting the Effect of Device Parameters on Performance of Nanoscale TFET. Number of vacancies: 4	<ul> <li>Food &amp; accommodation: Complimentary Certificate: Internship completion certificate.</li> <li>Office stationery: Complimentary office stationery kit including pendrive/ harddisk and consumables.</li> <li>Travel: Travelling Allowance (one time onward and return train ticket cost by shortest route to/from Silchar Railway station by 2S/ Sleeper class will be reimbursed as per TA norms).</li> </ul>

Contact Details: Dr. Kavicharan Mummaneni, Asst. Professor, Dept. of Electronics & Communication Engg., National Institute of Technology Silchar. Ph: +918919656541, Email: <u>kavicharan@ece.nits.ac.in</u>